

CLAIMS

What is claimed is:

1. A system for displaying images on a display, said system comprising:

a decoder for decoding encoded images and parameters associated with the images;

image buffers for storing the decoded images;

parameter buffers for storing the decoded parameters associated with the decoded images;

a display engine for receiving the decoded parameters and displaying the decoded images based on the decoded parameters; and

a display manager for determining the display order of the decoded images.

2. The system according to claim 1 wherein the system further comprises a first processor and a second processor.

3. The system according to claim 2 wherein the system further comprises a first memory and a second memory.

4. The system according to claim 2 wherein the first processor decodes encoded images and parameters associated with the images.

5. The system according to claim 2 wherein the first processor displays the decoded images based on the decoded parameters.

6. The system according to claim 2 wherein the second processor determines the display order of the decoded images.

7. The system according to claim 2 wherein the second processor is an off-chip processor.

8. The system according to claim 3 wherein the second memory is an off-chip memory.

9. The system according to claim 3 wherein the first memory is a SRAM.

10. The system according to claim 3 wherein the second memory is a DRAM.

11. The system according to claim 3 wherein the first memory stores a first set of instructions associated with decoding encoded images and parameters associated with the images.

12. The system according to claim 3 wherein the second memory stores the image buffers.

13. The system according to claim 3 wherein the second memory stores the parameter buffers.

14. The system according to claim 3 wherein the second memory stores a second set of instructions associated with displaying the encoded images.

15. A circuit for displaying images on a display, said system comprising:

a decoder;

image buffers connected to the decoder and configured to store images decoded by the decoder;

parameter buffers connected to the decoder and configured to store parameters associated with the images and decoded by the decoder;

a display engine connected to the image buffers and the parameter buffers and configured to receive the decoded parameters from the parameter buffers and display the decoded images based on the decoded parameters; and

a display manager connected to the display engine and configured to determine the display order for the decoded images based on the decoded parameters.

16. The circuit according to claim 15 wherein the display manager is connected to the image buffers and the parameter buffers.

17. The circuit according to claim 15 wherein the display engine is configured to display the decoded images in the order determined by the display manager.

18. The circuit according to claim 15 wherein a first processor is configured to decode the images and the associated parameters.

19. The circuit according to claim 18 wherein the first processor is configured to display the decoded images based on the decoded parameters.

20. The circuit according to claim 15 wherein a second processor is configured to determine the display order for the decoded images based on the decoded parameters.

21. The circuit according to claim 15 wherein a first memory is configured to store a first set of instructions configured to decode the images and the associated parameters.

22. The circuit according to claim 21 wherein the first memory is configured to store a second set of instructions configured to display the decoded images based on the decoded parameters.

23. The circuit according to claim 15 wherein a second memory is configured to store a set of instructions

configured to determine the display order of the decoded images based on the decoded parameters.

24. A method for displaying images, the method comprising:

- decoding images;
- decoding parameters associated with the images;
- buffering the decoded images;
- buffering the decoded parameters associated with the decoded images;
- determining the display order for the decoded images based on the associated decode parameters; and
- displaying the decoded images based on the associated decoded parameters and based on the determined display order.

25. The method according to claim 24 wherein a first memory stores a set of instructions configured to decode the images and the associated parameters.

26. The method according to claim 24 wherein a first processor decodes the images and the associated parameters.

27. The method according to claim 26 wherein the first processor buffers the decoded image and the decoded parameters to a second memory.

28. The method according to claim 24 wherein a second processor determines the display order of the decoded images based on the decoded parameters.